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U.S. Patent Application No.: 10/716,595
Attorney Docket No.: 57941.000062
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

: Group Art Unit: 2827

Michael Farmwald et al.

: Examiner: Tan Nguyen

Appln. No.: 10/716,595

: Confirmation No.: 7213

Filed: November 20, 2003

Customer No.: 21967

For: INTEGRATED CIRCUIT I/S

USING A HIGH PERFORMANCE BUS

INTERFACE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to individual to be material to patentability, Applicants hereby submit attached Form PTO-1449 (modified) listing cited This submission is made in accordance with 37 references. C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

The above-identified patent application is a member of a family of patents and patent applications descending from U.S. Patent Application No. 07/510,898. Some of the patents in this family are the subject of former and current litigations,

including: Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. 00-20905 RMW (N.D. Cal. 2000); Rambus Inc. v. Hynix Semiconductor, Inc. et al., No. CV 05-00334 RMW (N.D. Cal. 2005); Rambus Inc. v. Hynix Semiconductor, Inc. et al., No. 3:05cv334 (N.D. Cal. 2005); Rambus Inc. v. Samsung Electronics Co., Ltd. et al., No. CV 05-02298 RMW (N.D. Cal. 2005); Rambus Inc. v. Samsung Electronics Co., Ltd. et al., No. 4:05cv2298 (N.D. Cal. 2005); Rambus Inc. v. Micron Technology, Inc. et al., No. CV 06-00244 RMW (N.D. Cal. 2006); Micron Technology v. Rambus Inc., No. CV 00-792 KAJ (D. Del. 2000); Rambus Inc. v. Infineon Technologies et al., No. 3:00cv524 (E.D. Va. 2001); Alberta Telecommunications Research Centre v. Rambus Inc., No. 3:05cv00789 (E.D. Va. 2005); and Alberta Telecommunications Research Centre v. Rambus Inc., No. C-06-02595 RMW (N.D. Cal. 2006). Some of the patents in this family are also the subject reexamination proceedings before the U.S. Patent Trademark Office, including Reexamination Control Nos.: 95/001,026 (involving U.S. Patent No. 6,378,020); 95/001,008 (involving U.S. Patent No. 6,715,020); 95/001,013 (involving U.S. Patent No. 6,697,295); 95/000,166 (involving U.S. Patent 6,426,916); 95/000,178 (involving U.S. Patent No. 6,324,120); 95/000,183 (involving U.S. Patent No. 6,182,184); 95/000,250 (involving U.S. Patent No. 6,452,863); and 90/010,082

(involving U.S. Patent No. 6,038,195). The cited references have come to the attention of the associated individuals as a result of the aforementioned litigations and reexamination proceedings.

The cited references, while believed to be of some relevance, are not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. Applicants hereby expressly reserve the right to swear behind the effective dates of any of the cited references. Applicants further reserve the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this information disclosure statement. This information disclosure statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this information disclosure statement discloses only the best references of which the associated individuals are aware.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with

the next communication to the Applicants.

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 C.F.R. § 1.491; (iii) before the mailing date of a first Office Action on the merits for the above-identified patent

application; or (iv) before the mailing date of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

Respectfully submitted,

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